

Technical Program Schedule

Locations: All presentations are in the Chardonnay Room, posters and vendor tables are in the Fountain Court, and Training and Tutorial Sessions are in the Cabernet Room

SUNDAY		August 11	
10:30	6:30	Training and Tutorial Sessions (see detailed schedule)	
7:00	8:00	Welcome Reception in Fountain Court	
MONDAY		August 12	
		Opening and Welcome	
8:30	8:40	Sally Wood, Santa Clara University Program Overview and Workshop Retrospective	
8:40	9:00	John Treichler, Raytheon Applied Signal Technology Plenary Presentation: Large-scale video processing for YouTube	
9:00	10:00	Michele Covell, Senior Staff Research Scientist at Google Research	
10:00	10:20	Break	
10:20	11:50	Monday morning posters	
		1A	DSP Classification and Segmentation
		1B	DSP Compressive Sensing
		1C	DSP Embedded System and Testbed Design
			Novel Methods: Laboratory, Computer-based, Distance Learning
		1D	SPE Learning
12:00	1:00	Lunch	
		Plenary Presentation: DSP System Design Challenges	
1:15	2:15	James Truchard, President, CEO, and Cofounder of National Instruments	
2:15	4:00	Monday Afternoon Posters	
		2A	DSP Estimation
		2B	DSP Biomedical Signal Processing
		2C	DSP Graphical Models
2:45	3:00	break	
		Invited Talk: Predictable Impacts of the Development of Effective Signal Processing MOOCs	
4:00	4:30	C.S. Burrus, Dean emeritus, Rice University Invited Talk: Signal Processing Initiatives from the SPS Education Committee	
4:30	5:00	Douglas B Williams, Georgia Institute of Technology	
5:30		depart for banquet	
Tuesday		August 13	
		Plenary Presentation: Intelligent hyperspectral imaging and remote sensing from space using onboard digital signal processing	
8:30	9:30	Jeff Puschell, Principal Engineering Fellow at Raytheon Space and Airborne	

Systems

9:30	9:45	break		
9:45	11:15	Tuesday Morning Posters		
		3A	DSP	Image Processing
		3B	DSP	Blind Separation and Identification
		3C	SPE	Signal Processing Across the Engineering Curriculum
				Invited Talk: Digital pre-distortion of power amplifiers in cellular wireless systems
				Chris Dick, DSP Chief Architect, Communications and Signal Processing Group, Xilinx, Inc
11:15	11:45			Invited Talk: Large data, stream processing & project-based learning
11:45	12:15			Darel Linebarger, Mathworks
				Networking Afternoon: Training and Tutorial Sessions: Cabernet Special Workshops: Chardonnay
				Panel: Signal Processing at the University: Still an Active Research Area or 'Just' an Undergraduate Service Course?
7:00	8:00			Panelists: Ronald Schafer, Gene Frantz
8:00	8:15			break
8:15	9:45	Tuesday Evening Posters		
		4A	DSP	Communication Systems
		4B	DSP	Adaptive Filtering
		4C	DSP	Denosing and Artifact Removal
		4D	SPE	Signal Processing Education in Digital Communications

Wednesday

August 14

Plenary Presentation: [Computers that understand speech: Where are we? Where are we going?](#)

8:30	9:30			Roberto Pieraccini, CEO International Computer Science Institute
9:30	9:45			break
9:45	11:15	Wednesday Morning Posters		
		5A	DSP	Acoustic Analysis and Processing
		5B	DSP	Coding and Decoding
		5C	DSP	Filter and Equalizer Design
				Invited Talk: Transitions
11:15	11:45			Gene Frantz, Rice University
11:45	12:00	Closing		Sally Wood, Santa Clara University
12:00				box lunches

Embassy Suites Hotel Napa Valley Main Level Meeting & Event Space

